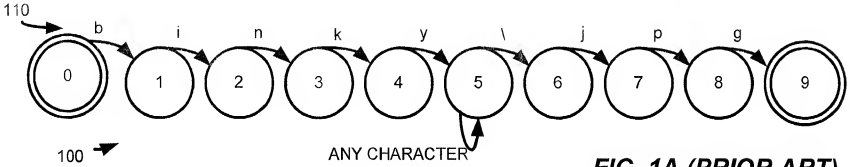

FIG. 1A (PRIOR ART)

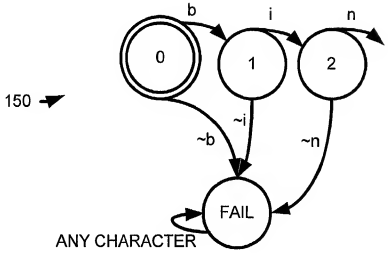

FIG. 1B (PRIOR ART)

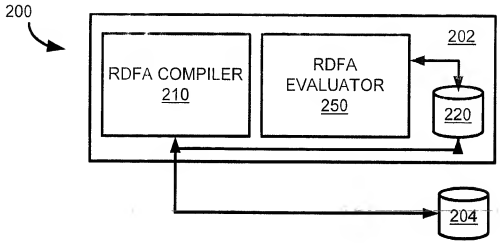
200
RDFA COMPILER
210
RDFA
EVALUATOR
250
202
220
204

FIG. 2A

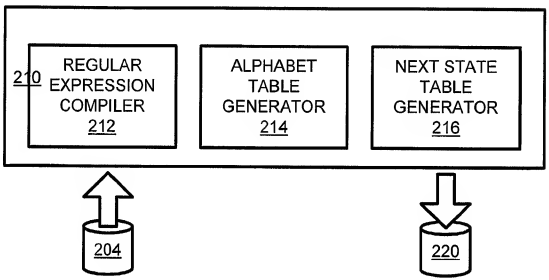
210
REGULAR
EXPRESSION
COMPILER
212
ALPHABET
TABLE
GENERATOR
214
NEXT STATE
TABLE
GENERATOR
216
204
220

FIG. 2B

TOEOST "2315000T

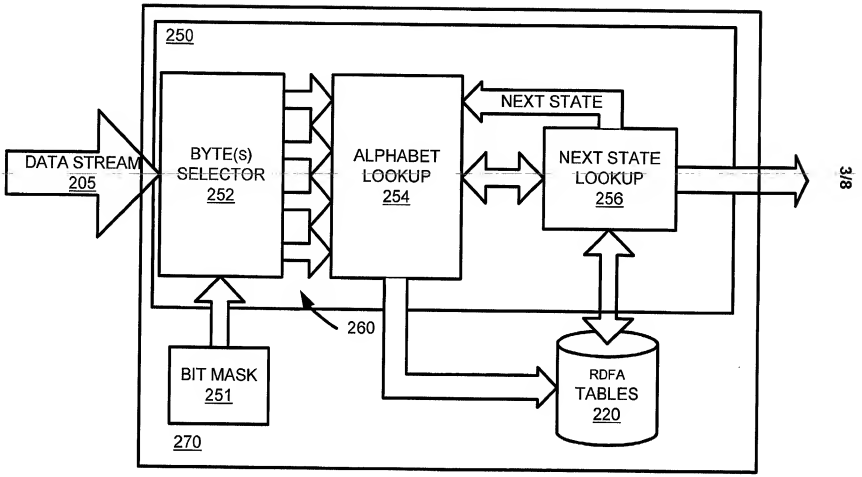

FIG. 2C

TOCOET: 29:50001

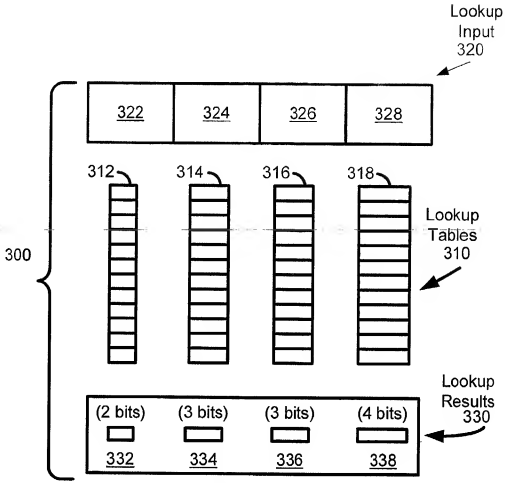

FIG. 3

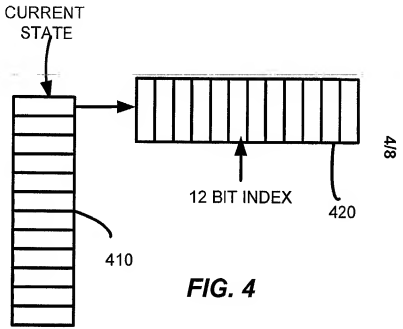

FIG. 4

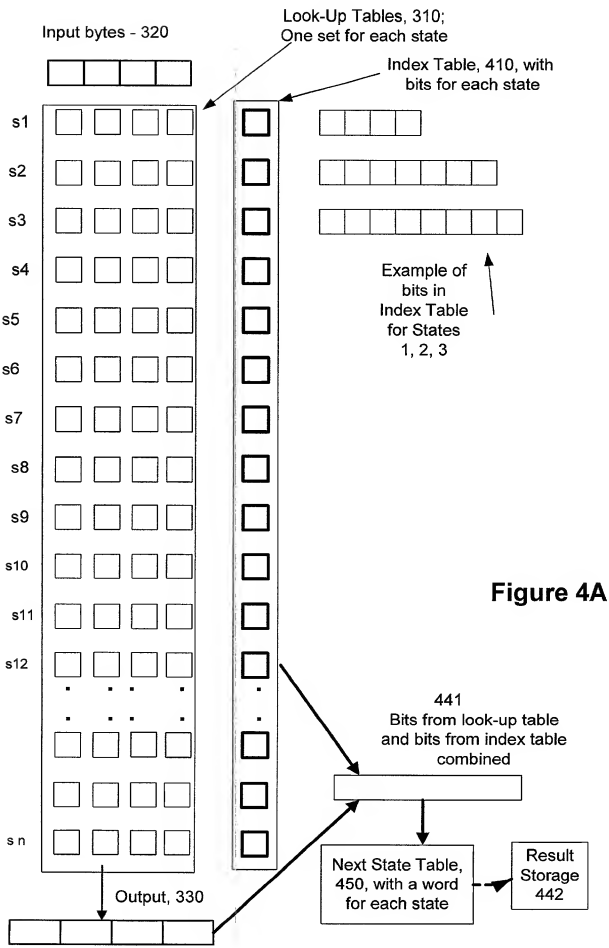
Input bytes - 320
Look-Up Tables, 310;
One set for each state
Index Table, 410, with
bits for each state
s1
s2
s3
s4
s5
s6
s7
s8
s9
s10
s11
s12
s n
Example of
bits in
Index Table
for States
1, 2, 3
441
Bits from look-up table
and bits from index table
combined
Next State Table,
450, with a word
for each state
Result
Storage
442
Output, 330

Figure 4A

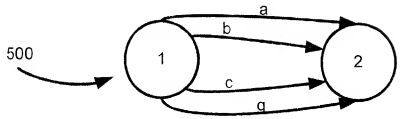
500
1
2
a
b
c
g

FIG. 5

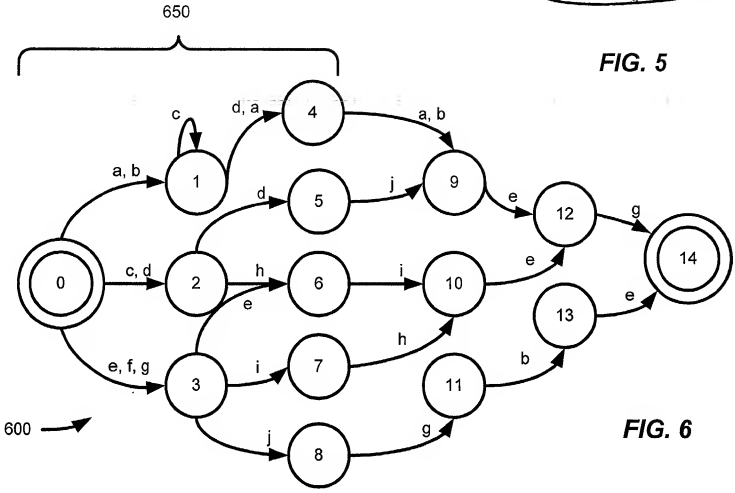
650
600
0
1
2
3
4
5
6
7
8
9
10
11
12
13
14
a, b
c
d, a
a, b
d
j
e
g
c, d
h
i
e
e, f, g
i
h
b
e
j
g

FIG. 6

Fig. 6A Illustration of number of states reachable by 2-closure.

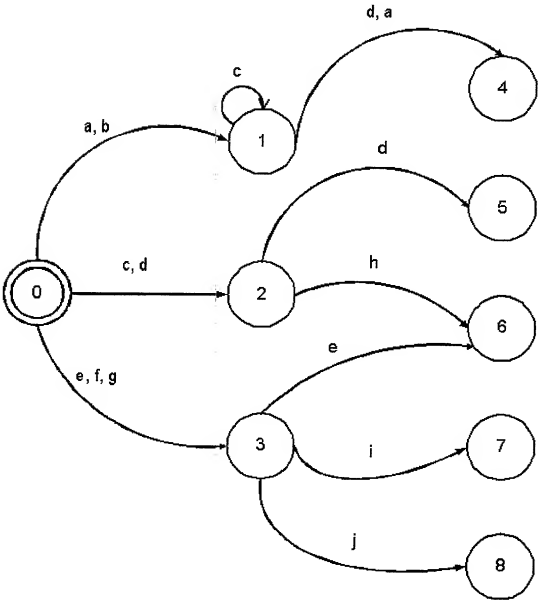

Fig. 7A DFA for processing 8 characters

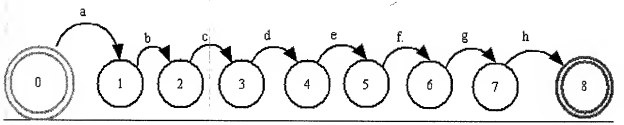
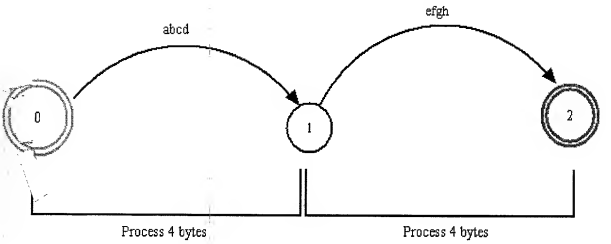
Fig 7B RDFA for processing 4 bytes in parallel.